

<b>Notice of References Cited</b>	Application/Control No. 10/753,446	Applicant(s)/Patent Under Reexamination MLAZGAR ET AL.	
	Examiner Jeffrey M. Rutkowski	Art Unit 2616	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,568,486	10-1996	Huscroft et al.	370/395.2
*	B	US-7,035,292	04-2006	Giorgetta et al.	370/509
*	C	US-7,139,286	11-2006	Akaike et al.	370/466
*	D	US-5,764,626	06-1998	VanDervort, Cole S.	370/232
*	E	US-7,139,271	11-2006	Parruck et al.	370/392
*	F	US-7,027,447	04-2006	Robertson et al.	370/395.62
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Venkataraman, R., "Designing SONET/ATM layer processing ASICs using embedded approach," Computers and Communications, 1995. Conference Proceedings of the 1995 IEEE Fourteenth Annual International Phoenix Conference on, vol., no., pp.437-443, 28-31 Mar 1995
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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